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August 2, 2007

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Re: CERTIFICATE OF CORRECTION

U. S. Letters Patent No. 7,007,156

Issued: February 28, 2006

For:

MULTIPLE COPROCESSOR ARCHITECTURE TO

PROCESS A PLURALITY OF SUBTASKS IN

PARALLEL Inventor: Stark et al.

Our File No. 42390.P9926

## Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent. This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TĄYŁOR & ZAFMAN LLP

Edwin H. Taylor

Reg. No. 25,129

EHT/sn Enclosures

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

**PATENT NO.** : 7,007,156

DATED : February 28, 2006

INVENTOR(S) : Stark, et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, at line 42, delete "this" and insert -- these--.

MAILING ADDRESS OF SENDER
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN
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PATENT NO. <u>7,007,156</u>

Certificate of Correction (PTO Form 1050)-Amended